

Applic. No. 10/008,241

Amdt. dated March 12, 2004

Reply to Office action of November 12, 2003

Claim Amendments

This listing of the claims will replace all prior versions,
and listings, of claims in the application:

Claim 1 (currently-amended): An electronic component,
comprising:

a semiconductor unit having:

a housing having a first side face; and

at least two terminal pins protruding out of said first
side face of said housing;

said housing having an isolation barrier formed outside said
housing between said two terminal pins, said isolation barrier
having a side remote from said first side face of said
housing, and said two terminal pins protruding beyond said
side of said isolation barrier.

Claim 2 (original): The component according to claim 1,
wherein said isolation barrier is integrally formed onto said
housing.

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Claim 3 (original): The component according to claim 2,
wherein:

said first side face has a first edge and a second edge
opposite said first edge; and

said isolation barrier is a plate extending on said first side
face from said first edge to said second edge.

Claim 4 (original): The component according to claim 1,
wherein:

said first side face has a first edge and a second edge
opposite said first edge; and

said isolation barrier is a plate extending on said first side
face from said first edge to said second edge.

Claim 5 (cancelled)

Claim 6 (original): The component according to claim 1,
wherein said housing has at least one side wall that together
with said isolation barrier encloses said two terminal pins
from at least three sides.

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Claim 7 (original): The component according to claim 1,
wherein said at least two terminal pins protrude out of said
housing next to each other in a row.

Claim 8 (original): The component according to claim 1,
wherein:

said first side face has a first edge and a second edge
opposite said first edge;

a first one of said two terminal pins is located closer to
said first edge than a second one of said two terminal pins;
and

said second one of said two terminal pins is located closer to
said second edge than said first one of said two terminal
pins.

Claim 9 (currently-amended): ~~The component according to claim~~
~~1, comprising:~~

~~a plurality of isolation barriers that includes said isolation~~
~~barrier;~~

An electronic component, comprising:

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a semiconductor unit having:

a housing having a first side face and side walls; and

at least two terminal pins protruding out of said first
side face of said housing;

said housing having a plurality of isolation barriers formed
outside said housing, each of said isolation barriers being
disposed between a respective two of said at least two
terminal pins;

at least some of said housing having side walls of said
housing that together with at least one of said plurality of
said isolation barriers surrounds surrounding at least one of
said at least two terminal pins from four sides.

Claim 10 (cancelled)

Claim 11 (currently-amended): A transistor, comprising:

a semiconductor unit having:

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a housing having a first side face; and

at least two terminal pins protruding out of said first
side face of said housing;

said housing having an isolation barrier formed outside said
housing between said two terminal pins, said isolation barrier
having a side remote from said first side face of said
housing, and said two terminal pins protruding beyond said
side of said isolation barrier.

Claim 12 (new): A transistor, comprising:

a semiconductor unit having:

a housing having a first side face and side walls; and

at least two terminal pins protruding out of said first
side face of said housing;

said housing having a plurality of isolation barriers formed
outside said housing, each of said isolation barriers being
disposed between a respective two of said at least two
terminal pins;

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at least some of said side walls of said housing together with
at least one of said plurality of said isolation barriers
surrounding at least one of said at least two terminal pins
from four sides.